

Substitute for Form 1449 A & B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				<i>Complete if Known</i>	
				Application Number	Divisional of 08/866,331 (10/674 385)
				Confirmation Number	Not yet assigned
				Filing Date	October 1, 2003
				First Named Inventor	Hiroshi TANABE
				Art Unit	2822 (Parent)
				Examiner Name	Not yet assigned
Sheet	1	of	2	Attorney Docket Number	Q77312

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		Number	Kind Code ² (if known)		
X		US 5,578,520	A	11-26-1996	Zhang et al.
		US 5,043,299		08-27-1991	Chang et al.
		US 4,529,617		07-16-1985	Chenevas-Paule et el.
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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Translation ⁶
		Country Code ³	Number ⁴	Kind Code ⁵ (if known)			
X	X	X	X	X	X	X	X

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X	X	X	X	X	X	X	X

Examiner Signature	<i>[Signature]</i>	Date Considered	2/24/05
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Sheet	2	of	2		

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		Number	Kind Code ² (if known)		
<i>fu</i>	X	US 6,261,877	B1	07-17-2001	Yamazaki et al.
X	X	US 6,124,154	A	09-26-2000	Miyasaka
X	X	X	X	X	X

FOREIGN PATENT DOCUMENTS							
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<i>fu</i>	X	JP	5-211167	A	08-20-1993	X	X
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X	X	JP	58-201326	A	11-24-1983		

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	Translation ⁶
<i>fu</i>	X	M. SEKIYA ET AL., "High Performance Poly-Crystalline Silicon Thin Film Transistors Fabricated ... Vapor Deposition of SiO ₂ ", IEEE Electron Device Letters, Vol. 15, No. 2, February 1994, pp. 69-71	X
X	X	X	X

Examiner Signature	<i>fuore</i>	Date Considered	2/24/05
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